

High-Frequency Noise in RF Active CMOS Mixers

Payam Heydari
 Department of EECS
 University of California, Irvine
 Irvine 92697-2625

Abstract - A new analytical model for high-frequency noise in RF active CMOS mixers such as single-balanced and double-balanced architectures is presented. The analysis includes the contribution of non-white gate-induced noise at the output as well as the spot noise figure (NF) of the RF CMOS mixer, while accounting for the non-zero correlation between the gate-induced noise and the channel thermal noise. The noise contribution of the RF transconductor as well as the switching pair on the output noise is discussed. The analytical model predicts that the output noise and NF are both a strong function of the LO frequency at gigahertz range of frequencies. Simulation results verify the accuracy of the analytical model.

1. INTRODUCTION

Mixers in radio-frequency (RF) transceivers are considered as nonlinear blocks. The frequency-varying bias-points of active switching devices due to the large-signal behavior of mixers cause the mixer noise to become essentially a cyclostationary stochastic process. In addition, the wireless standards such as bluetooth and IEEE802.11b operating in multi-gigahertz range demand front-end circuits including the low-noise amplifier (LNA) and the mixer to operate at giga-hertz range of frequencies. The low-frequency analytical models for the mixer noise proposed in [1] and [2] are, therefore, incapable of accurately predicting the noise characteristics of the mixer such as the noise figure (NF), the output noise, or the input-referred noise. More precisely, at RF frequencies, the random potential fluctuations in the conducting channel of a MOS transistor are coupled to the gate terminal through the oxide capacitance and cause a gate noise current even all terminal voltages of the MOS device are fixed. Known as gate-induced noise, this noise source is correlated to the channel thermal noise and its power spectral density (PSD) increases with frequency, hence it is a non-white random process. [3] proposed a stochastic differential equation approach to characterize the noise in CMOS switching mixers. [3] was able to consider the cyclostationary noise generation in mixers. However, this model cannot efficiently analyze the noise in an active mixer in which the switching pair is mostly operating in the triode region whereas the RF transconductor operates in the saturation region.

In this paper we present a high-frequency noise model for the active CMOS mixers while considering the short-channel effects in MOS devices in nanometer technologies. The proposed analytical model is simplified to the noise model presented in [1] at low frequencies. Closed-form expressions resulting from the analytical model gives circuit designers an understanding of how the high-frequency noise affects the mixer NF and output noise.

Section 2 briefly reviews single and double-balanced mixers, and the dominant device noise sources in high-frequencies. Section 3 presents the noise analysis of the CMOS mixer and illustrates the noise contribution of the transconductor and switching

pair on the output noise and the NF. Simulation results are provided in Section 4.

2. BACKGROUND

A. Large-Signal Analytical Model for RF CMOS Mixers

Figs 1 (a) and (b) depict the double-balanced and single-balanced cells, respectively, that are extensively used to implement active switching mixers.

To achieve an accurate analytical model for an RF CMOS mixer in nanometer technology, the I-V characteristics of a short-channel device is utilized [4]:

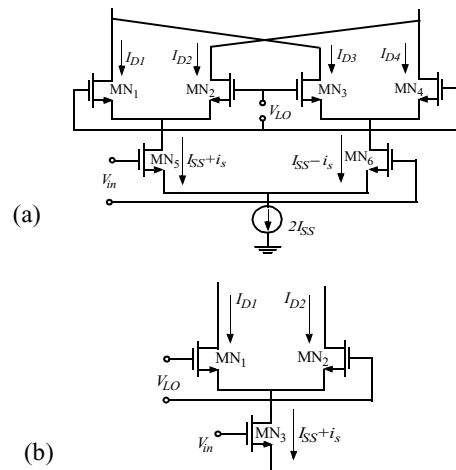


Fig. 1. Active blocks used in switching mixers, (a) a CMOS Gilbert cell. (b) a differential pair.

$$I_D = W v_{sat} C_{ox} \frac{(V_{GS} - V_{TH})^2}{V_{GS} - V_{TH} + L E_{sat}} \quad (1)$$

where W and L are the transistor channel width and length, respectively, C_{ox} is the gate-oxide capacitance, v_{sat} is the saturated velocity, and E_{sat} is the saturated electric field. Using Eq. (1), the ratio of the instantaneous transconductance g_m of a short-channel transistor in terms of its DC bias current I_{DQ} is:

$$g_m = \frac{2I_{DQ}}{V_{GS} - V_{TH}} \left[\frac{(V_{GS} - V_{TH})/2 + L E_{sat}}{V_{GS} - V_{TH} + L E_{sat}} \right] \quad (2)$$

Since a large LO signal is applied to the switching transistors, the bias points of MN1 and MN2 are not fixed but vary periodically [1]. The output current of the single-balanced mixer shown in Fig. 1 (b) is a function of the instantaneous LO voltage V_{LO} and the current at the output of the tail current $I_{SS} + i_s$. Therefore, we have:

$$I_o = I_{D1} - I_{D2} = F(V_{LO}, I_{SS} + i_s) = F(V_{LO}, I_{SS}) + \frac{\partial F(V_{LO}, I_{SS})}{\partial (I_{SS} + i_s)} i_s$$

or

$$I_o = p_0(t) + p_1(t)i_s \quad (3)$$

where $p_0(t)$ and $p_1(t)$ are two periodic waveforms [1]. In a Gilbert-cell double-balanced mixer, $p_0(t)$ is eliminated because of the presence of two stages of differential signaling. When the absolute value of the LO voltage is less than a certain voltage, V_x , the tail current is shared between the two switching devices MN1 and MN2. Otherwise, only one stage turns on and the absolute value of $p_1(t)$ thus becomes one, as shown in Fig. 2.

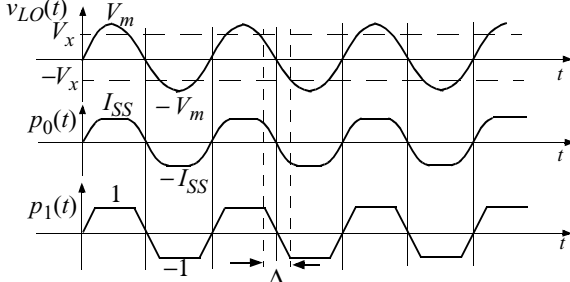


Fig. 2. $p_0(t)$ and $p_1(t)$ waveforms [1].

B. High-Frequency Noise in MOSFETs

The ohmic regions of a MOSFET exhibit finite resistivity, thereby contributing thermal noise. In RF integrated circuits, devices with very large channel widths are used. In such cases, the source and drain ohmic resistances are negligible whereas the gate resistance can be significant. The gate resistance forms a distributed RC circuit. However, it can be shown that the distributed effect of the gate resistance in the noise model can be approximated using a lumped resistance whose value is one-third of the physical ohmic gate resistance [5]. The thermal noise due to the ohmic poly resistance is thus as follows:

$$\overline{V_{nrg}^2} = 4KT \frac{r_G}{3} \quad (5)$$

In addition, the thermal fluctuations of channel charge in the MOSFET produce effects that are modeled by drain and gate current noise sources. These current noise sources are partially correlated with each other, because they share a common physical origin and thus possess a spectral power given by:

$$\overline{i_{nd}^2} = 4KT\gamma g_m \quad (6)$$

$$\overline{i_{ng}^2} = 4KT\delta g_g \quad (7)$$

$$c = \frac{\overline{i_{ng} i_{nd}^*}}{\sqrt{\overline{i_{ng}^2} \overline{i_{nd}^2}}} \quad (8)$$

where $g_g = \zeta(\omega^2 C_{GS}^2)/g_m$ is the real-part of gate-to-source admittance, and γ , δ , and c are bias-dependent factors. For short-channel MOSFETs, γ and δ are 2.5, and 3, respectively. Because the value of the correlation coefficient, c , in the short-channel regime is not known at present, we will assume that c remains at its long-channel value, which is $j0.395$ [6].

3. NOISE ANALYSIS

The output noise generated in a mixer has a periodically time-varying statistics. This is because an active mixer with a periodic LO is essentially a nonlinear periodic system and its response to an input stationary noise source is a non-stationary process whose statistical properties are invariant to the time-shift by integer multiples of T_{LO} , hence it is modeled as a

cyclostationary stochastic process. In a cyclostationary process, the mean and autocorrelation function are both periodic. As a consequence, the power spectral density (PSD) is time-varying.

There are three major noise sources that contribute to the noise at the output of the mixer; noise from the transconductor, noise from the switching stage, and noise coming from LO. Note that each of these three major noise sources can be a combination of constituent device noise sources. A comprehensive **high-frequency** noise analysis of the RF CMOS mixer in the presence of the three noise sources is given in the following. Throughout the forthcoming analysis, we assume that $\Delta f = 1\text{Hz}$ to simplify the noise expressions.

A. Noise from the RF Transconductor

Fig. 3 (a) shows a single-balanced cell along with the input-referred noise sources due to the noise contribution of MN3 at the input of the transconductor. From a system-level perspective, the noise signal at the transconductor is multiplied by the switching pair's instantaneous current gain $p_1(t)$ and generates a current noise $i_{o3}(t)$, as shown in Fig. 3 (b).

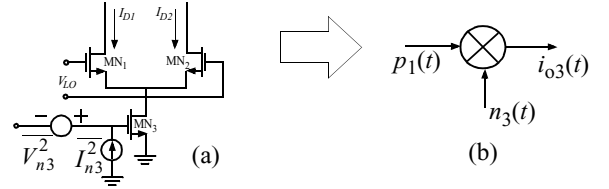


Fig. 3. (a) Noise contribution of the transconductor in a single-balanced mixer, (b) mixer operation for the transconductor's noise.

$$i_{o3}(t) = n_3(t)p_1(t) \quad (9)$$

$i_{o3}(t)$ is thus a cyclostationary process due to the fact that $p_1(t)$ is a periodic function and $n_3(t)$ is a wide-sense stationary process. Subsequently, the output noise PSD of the mixer due to the noise from the transconductor is a time-varying function. However, the *time-average* of the output noise $i_{o3}(t)$ is a WSS process [7]. The PSD of this time-average process is [1]:

$$S_{n_3}^o(\omega, t) = \sum_{n=-\infty}^{\infty} |p_{1,n}|^2 S_{n_3}(\omega - n\omega_{LO}) \quad (10)$$

$S_{n_3}(\omega)$ (which is equal to $\overline{i_{n_3}^2}$ for $\Delta f = 1\text{Hz}$) is the PSD of the output current noise generated in MN3 whose value is yet to be determined.

To derive $S_{n_3}(\omega)$, the impact of various noise sources must be accounted for. The noise sources contributing on the output noise contain two correlated thermal noise sources, the drain current noise and the gate-induced noise; and uncorrelated noise sources containing the thermal noise of ohmic polysilicon gate, the thermal noise of input source resistance, and the flicker noise. Fig. 4 depicts the high-frequency small-signal model including all these noise sources:

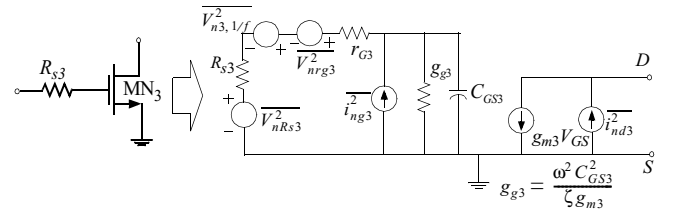


Fig. 4. Noise contribution of the transconductor.

To quantify the effect of noise at drain node of MN3, the contribution of uncorrelated noise sources are first considered separately. The overall output noise due uncorrelated noise sources is then calculated using superposition. The three uncorrelated noise sources in Fig. 4 are thermal noise of ohmic polysilicon gate r_{G3} , the thermal noise of input source resistance R_{s3} , and the flicker noise. The noise at the output of MN3 due to each uncorrelated noise source is obtained by multiplying its PSD with the square of the transfer function. Table 1 summarizes the derivations for thermal noise of ohmic polysilicon gate and the thermal noise of input source resistance. In the derivation of expressions in Table 1, it is assumed that $(r_{G3}+R_{s3}) \ll g_{m3}/(\zeta\omega^2 C_{GS3}^2)$.

Table 1: Input noise sources and the corresponding PSD of the noise responses at the output of MN3.

Input noise source	PSD of the noise at the output
$\overline{V_{nrG3}^2}$	$\overline{V_{nrG3}^2} \frac{g_{m3}^2}{[\omega C_{GS3}(r_{G3} + R_{s3})]^2 + 1}$
$\overline{V_{nrS3}^2}$	$\overline{V_{nrS3}^2} \frac{g_{m3}^2}{[\omega C_{GS3}(r_{G3} + R_{s3})]^2 + 1}$

The overall output noise due to the noise sources, $\overline{V_{nrG3}^2}$ and $\overline{V_{nrS3}^2}$, is frequency-dependent, which, in turn, results in a non-white noise at the output of the transistor. To simplify the analytical model, we obtain an *equivalent white noise* process for this *colored-noise* process at the output by averaging the noise in the frequency domain over the LO frequency:

$$S_{n3}^u(\omega) \approx \frac{(\overline{V_{nrG3}^2} + \overline{V_{nrS3}^2})}{f_{LO}} \int_0^{f_{LO}} \frac{g_{m3}^2}{[\omega C_{GS3}(r_{G3} + R_{s3})]^2 + 1} df \quad (11)$$

Calculating the above integral, the overall noise due to the uncorrelated noise sources, therefore, is as follows:

$$S_{n3}^u(\omega) = 4KTg_{m3}^2 R_{sG3}' \left(\frac{\omega_{p3}}{\omega_{LO}} \right) \tan^{-1} \left(\frac{\omega_{LO}}{\omega_{p3}} \right) \quad (12)$$

where $R_{sG3}' = r_{G3}/3 + R_{s3}$, $R_{sG3} = r_{G3} + R_{s3}$, and $\omega_{p3} \equiv (C_{GS3} R_{sG3})^{-1}$. For the LO frequency much smaller than ω_0 , (12) is simplified to $S_{n3}^u(\omega) = 4KTg_{m3}^2 R_{sG3}'$.

As for the flicker noise, it appears at the output of the mixer around f_{LO} and all the odd-harmonics. The PSD of the flicker noise at the output of the transconductor is found using equations similar to (11) and (12) with the consideration that the flicker noise is a colored noise source.

To account for the partially correlated noise sources, a more complicated analysis needs to be undertaken. Recall that the gate induced noise and drain noise are partially correlated stochastic processes with a correlation coefficient given by (8). This non-zero correlation between the noise sources creates an intricacy in the derivation of the PSD of the output current noise, because the superposition principle is not held valid for the power of correlated stochastic processes. To perform the noise analysis of partially correlated current noise sources, i_{ng3} and i_{nd3} , the gate induced noise is first expressed as a superposition of its correlated and uncorrelated components [6], i.e.,

$\overline{i_{ng3}^2} = \overline{i_{ng3,u}^2} + \overline{i_{ng3,c}^2} = 4KT\delta g_{g3}(1 - |c|^2) + 4KT\delta g_{g3}|c|^2$ (13) where c is given by (8). The uncorrelated component, $i_{ng3,u}$, is treated in the conventional way by obtaining the circuit response using the small-signal transfer function of MN3. Assuming $(r_{G3}+R_{s3}) \ll g_{m3}/\zeta\omega^2 C_{GS3}^2$, we have:

$$S_{n3}(\omega)|_{i_{ng3,u}} = \left(\frac{g_{m3}^2 R_{sG3}^2}{1 + \omega^2 C_{GS3}^2 R_{sG3}^2} \right) \overline{i_{ng3,u}^2} \quad (14)$$

The noise contributions of correlated current noise sources, $i_{ng3,c}$ and i_{nd3} on the overall PSD of the drain current of MN3 are determined by obtaining the current response to each individual current noise source, and then computing the mean-square of the resulting summation of these two individual responses. The channel noise source i_{nd3} directly appears at drain node of MN3. As for $i_{ng3,c}$, first, the output current of MN3 in response to this current source is obtained. $i_{ng3,c}$ is fully correlated with i_{nd3} . Therefore, it is treated as a current source proportional to i_{nd3} , i.e.,

$$\mathbf{F}\{i_{ng3,c}\} = \frac{j\omega C_{GS3}}{g_{m3}} |c| \sqrt{\frac{\delta\zeta}{\gamma}} \mathbf{F}\{i_{nd3}\} = \kappa_c \frac{j\omega C_{GS3}}{g_{m3}} \mathbf{F}\{i_{nd3}\} \quad (15)$$

where $\mathbf{F}\{\cdot\}$ represents the Fourier transformation. The total noise current at the drain of MN3 due to $i_{ng3,c}$ and i_{nd3} is readily found using small-signal high-frequency circuit for the transistor shown in Fig. 4:

$$\overline{i_{nc,MN3}^2} = \mathbf{F} \left\{ \overline{[(\kappa_c + 1)i_{nd3} + \kappa_c(i_{nd3} * h_{MN3}(t))]^2} \right\} \quad (16)$$

where $h_{MN3}(t) = 1/\tau_{G3} \exp(-t/\tau_{G3})$, and $\tau_{G3} = C_{GS3} R_{sG3}$. Also, $x(t)*y(t)$ stands for the convolution of x and y . Using stochastic analysis (the details are omitted due to the lack of space), the overall PSD of the current noise due to the correlated current sources $i_{ng3,c}$ and i_{nd3} is as follows:

$$S_{n3}(\omega)|_{i_{ng3,c},i_{nd3}} = \left[\frac{1 + (\kappa_c + 1)^2 \omega^2 C_{GS3}^2 R_{sG3}^2}{1 + \omega^2 C_{GS3}^2 R_{sG3}^2} \right] \overline{i_{nd3}^2} \quad (17)$$

Combining (13) and (17) yields the overall PSD of partially correlated noise sources i_{ng3} and i_{nd3} :

$$S_{n3}(\omega)|_{i_{ng3},i_{nd3}} = \left[\frac{1 + [(\kappa_c + 1)^2 + \kappa_c^2 \left(\frac{1}{|c|^2} - 1 \right)] \omega^2 C_{GS3}^2 R_{sG3}^2}{1 + \omega^2 C_{GS3}^2 R_{sG3}^2} \right] 4KT\gamma g_{m3} \quad (18)$$

A plot of $S_{n3}(\omega)|_{i_{ng3},i_{nd3}}$ vs. frequency is depicted in Fig. 5.

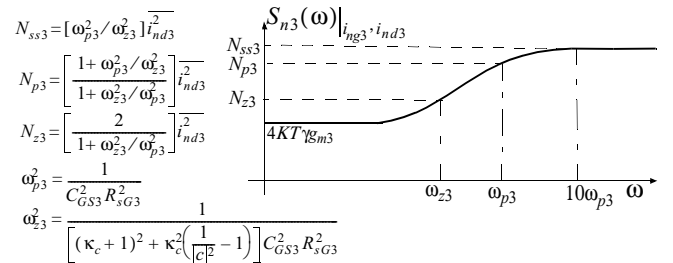


Fig. 5. The PSD of $S_{n3}(\omega)|_{i_{ng3},i_{nd3}}$ vs. the radian frequency

Direct substitution of $S_{n3}(\omega)|_{i_{ng3},i_{nd3}}$ in Eq. (10) yields a periodic expansion of $S_{n3}(\omega)|_{i_{ng3},i_{nd3}}$ with a fundamental period $1/f_{LO}$. However, $S_{n3}(\omega)|_{i_{ng3},i_{nd3}}$ is frequency-dependent, and hence, a direct substitution does not result in a closed-form expression for the output current noise of the transconductor. To alleviate this problem, we find an equivalent white noise process over the LO frequency for the colored noise component of $S_{n3}(\omega)|_{i_{ng3},i_{nd3}}$. The average power of this equivalent white noise process must be equal to the total area under the colored noise PSD in the interval $[0, f_{LO}]$:

$$\begin{aligned}
S_{n3}^c(\omega) &= \frac{1}{\omega_{LO}} \left(\int_0^{\omega_{LO}} S_{n3}(\omega) \Big|_{i_{ng3}, i_{nd3}} d\omega \right) \\
&= 4KT\gamma g_{m3} \left[\left(\frac{\omega_{p3}^2}{\omega_{z3}^2} \right) + \left(1 - \left(\frac{\omega_{p3}^2}{\omega_{z3}^2} \right) \right) \left(\frac{\omega_{p3}}{\omega_{LO}} \right) \tan^{-1} \left(\frac{\omega_{LO}}{\omega_{p3}} \right) \right] \quad (19)
\end{aligned}$$

where ω_{p3} and ω_{z3} , defined in Fig. 5, specify the pole and zero of $S_{n3}(\omega) \Big|_{i_{ng}, i_{nd}}$, respectively. As expected, the gate-induced noise has a high frequency contribution on the overall noise at the drain node of the transistor. Ignoring the gate-induced noise results in high-frequency error in the noise estimation. In fact, experimental results given in Section 4, verify this observation. The overall PSD of the noise at the output of MN3 is a summation of the output noise due to the correlated noise sources and the output noise due to the uncorrelated noise sources:

$$\begin{aligned}
S_{n3}(\omega) &= S_{n3}^c(\omega) + S_{n3}^u(\omega) \\
&= 4KTg_{m3} \left\{ \gamma \left(\frac{\omega_{p3}^2}{\omega_{z3}^2} \right) + \left[g_{m3} R'_{sG3} + \gamma \left(1 - \left(\frac{\omega_{p3}^2}{\omega_{z3}^2} \right) \right) \right] \left(\frac{\omega_{p3}}{\omega_{LO}} \right) \tan^{-1} \left(\frac{\omega_{LO}}{\omega_{p3}} \right) \right\} \quad (20)
\end{aligned}$$

As a consequence, $S_{n3}(\omega - n\omega_{LO})$ in Eq. (10) becomes frequency-independent. $\overline{S_{n3}^o(\omega, t)}$ is thus simplified to:

$$\overline{S_{n3}^o(\omega, t)} = S_{n3}(\omega) \sum_{n=-\infty}^{\infty} |p_{1,n}|^2 = E_{p1} S_{n3}(\omega) \quad (21)$$

where $S_{n3}(\omega)$ is given by Eq. (20), and E_{p1} is the energy of the Fourier series coefficients of the periodic signal $p_1(t)$ and is determined using the Parseval's relation [8]:

$$E_{p1} = \sum_{n=-\infty}^{\infty} |p_{1,n}|^2 = \frac{1}{T_{LO}} \int_0^{T_{LO}} [p_1(t)]^2 dt \quad (22)$$

For large LO amplitudes, $p_1(t)$ is a periodic square-wave waveform and the average energy per period becomes one.

The overall time-average output noise for the single-balanced mixer shown in Fig. 1 (b) is given by (21). A double-balanced mixer implemented by the Gilbert-cell, should be noisier than a single-balanced mixer because it uses more transistors. A simple extension of the above analysis to the double-balanced mixer results in the following noise expression for the double-balanced mixer of Fig. 1(a):

$$\overline{S_{n56}^o(\omega, t)} = E_{p1} S_{n56}(\omega) = 2E_{p1} S_{n3}(\omega) - 4KTg_{m3}^2 R_{s3} \quad (23)$$

B. Noise from the Switching Pair

The input to the switching pair of an RF mixer is a large LO signal. For a proper circuit operation we assume that $V_{LO} < \bar{V}_x$ (see Fig. 2). Therefore, the current generated by the tail current is shared between the two transistors, and the switching pair stay in the saturation during the entire transition. To capture the high-frequency noise behavior of the differential pair in an RF balanced mixer, the gate induced noise and ohmic gate noise are included in the noise model. When MN1 or MN2 is off, the output current is entirely determined by the current of the tail current and the switching pair does not contribute to the output noise. The noise contributions of MN1 and MN2 become significant during the time interval Δ indicated in Fig. 2, when the LO signal experiences a signal transition. The overall current noise is a superposition of the individual output noise current associated with each branch side of the differential circuit [1]. The underlying analysis focuses on I_{D1} , noting that a similar analysis can be done for I_{D2} . Similar to the analysis that was undertaken to study the noise behavior of the transconductor, the correlation between the gate-induced noise and the channel noise must be accounted

for. Solving the noise equations for the switching pair gives rise to the following equations:

$$\overline{I_{D1}^2} = \sum_{j=1}^2 \left(\overline{I_{D1}^2} \Big|_{R_{sGj}} + \overline{I_{D1}^2} \Big|_{i_{ngj,c}, i_{ndj}} + \overline{I_{D1}^2} \Big|_{i_{ngj,u}} \right) \quad (24)$$

With the aid of the high-frequency circuit model for the transistor, the frequency-average of each term in (24) is:

$$\overline{I_{D1}^2} \Big|_{i_{ng}, i_{nd}} = \frac{4KT\gamma}{g_{mi}} \left(\frac{g_{mi}g_{mj}}{g_{mi}+g_{mj}} \right)^2 \left[\left(\frac{\omega_{p12}^2}{\omega_{z12}^2} \right) + \left(1 - \left(\frac{\omega_{p12}^2}{\omega_{z12}^2} \right) \right) \left(\frac{\omega_{p12}}{\omega_{LO}} \right) \tan^{-1} \left(\frac{\omega_{LO}}{\omega_{p12}} \right) \right] \quad (25)$$

$$\overline{I_{D1}^2} \Big|_{R_{sG12}} = 4KTR'_{sG12} \left(\frac{g_{mi}g_{mj}}{g_{mi}+g_{mj}} \right)^2 \left(\frac{\omega_{p12}}{\omega_{LO}} \right) \tan^{-1} \left(\frac{\omega_{LO}}{\omega_{p12}} \right) \quad (26)$$

for $i, j = \{1, 2\}$ and $i \neq j$

where

$$\begin{aligned}
\omega_{p12} &= 1/(R_{sG12} C_{GS12}), \\
\omega_{z12}^2 &= \frac{1}{\left[(\kappa_c + 1)^2 + \kappa_c^2 \left(\frac{1}{|C|^2} - 1 \right) \right] C_{GS12}^2 R_{sG12}^2}
\end{aligned}$$

Similarly, the noise PSD at I_{D2} is calculated. The output noise is the sum of the PSD at I_{D1} and I_{D2} , which leads to the following:

$$\begin{aligned}
S_{n12}^o(\omega, t) &= \overline{I_{D1}^2} + \overline{I_{D2}^2} = \\
&8KT \left(\frac{g_{m1}g_{m2}}{g_{m1}+g_{m2}} \right) \left\{ \gamma \left(\frac{\omega_{p12}^2}{\omega_{z12}^2} \right) + \left[\left(\frac{2g_{m1}g_{m2}}{g_{m1}+g_{m2}} \right) R'_{sG12} + \gamma \left(1 - \left(\frac{\omega_{p12}^2}{\omega_{z12}^2} \right) \right) \right] \left(\frac{\omega_{p12}}{\omega_{LO}} \right) \tan^{-1} \left(\frac{\omega_{LO}}{\omega_{p12}} \right) \right\} \quad (27)
\end{aligned}$$

Recall that g_{m1} and g_{m2} are the instantaneous small-signal transconductances of switching devices MN1 and MN2 in Fig. 1 (b). g_{m1} and g_{m2} are periodic signals with a cycle time of $1/f_{LO}$. The transconductance of the pair, $G(t) = 2g_{m1}g_{m2}/(g_{m1}+g_{m2})$, will thus be a periodic function making $S_{n12}^o(\omega, t)$ to be a periodic function of time. The time-average PSD of $S_{n12}^o(\omega, t)$ gives a time-invariant PSD of the switching pair, i.e.,

$$\begin{aligned}
\overline{S_{n12}^o(\omega, t)} &\equiv \\
&4KT \left(\frac{2I_{SS}}{\pi V_0} \right) \left\{ \gamma \left(\frac{\omega_{p12}^2}{\omega_{z12}^2} \right) + \left[\left(\frac{1.16I_{SS}}{\pi V_x} \right) R'_{sG12} + \gamma \left(1 - \left(\frac{\omega_{p12}^2}{\omega_{z12}^2} \right) \right) \right] \left(\frac{\omega_{p12}}{\omega_{LO}} \right) \tan^{-1} \left(\frac{\omega_{LO}}{\omega_{p12}} \right) \right\} \quad (28)
\end{aligned}$$

The output noise contribution of the double-balanced mixer is twice the value given by (28).

To obtain the flicker noise contribution from the switching pair, a similar analysis is performed with the consideration that the flicker noise is a colored-noise source.

The noise at the output of the local oscillator has a cyclo-stationary behavior. This is due to the fact that LO is a periodically time-varying circuit. The device noise sources in an oscillator are directly contributing to the phase and amplitude noise of an oscillator [9]. As a simplified treatment, it is assumed that the noise present at the LO port is stationary. The time-average PSD of the current noise at the output of the mixer due to this noise component is:

$$\overline{S_{nLO}^o(\omega)} \equiv 4KT(R_{LO} + 2r_{G1}) \overline{G^2(t)} = 4KT(R_{LO} + 2r_{G1}) \left(\frac{2.32I_{SS}^2}{\pi V_0 V_x} \right) \quad (29)$$

where R_{LO} is the equivalent noise resistance and r_{G1} is the physical poly gate resistance.

C. Noise Figure

In RF mixers, the image signal does not carry useful information. Therefore, single-sideband noise figure (SSB NF) is preferred to double-sideband noise figure (DSB NF). Employing the SSB NF definition [10], and using equations (22), (28), and (29) in the SSB NF noise definition result in the following NF equations for single and double-balanced mixers:

$$\begin{aligned}
NF_{SSB} &= \frac{\int_0^{\infty} \overline{S_n^o(\omega, t)} d\omega}{g_c^2} \cdot \frac{1}{4KTR_s} \\
&= \frac{\overline{S_{n3}^o(\omega, t)} + \overline{S_{n12}^o(\omega, t)} + \overline{S_{nLO}^o(\omega, t)} + 4KT/R_L}{g_c^2} \cdot \frac{1}{4KTR_s} \quad \text{Single-balanced} \\
&= \frac{\overline{S_{n56}^o(\omega, t)} + 2\overline{S_{n12}^o(\omega, t)} + 2\overline{S_{nLO}^o(\omega, t)} + 4KT/R_L}{g_c^2} \cdot \frac{1}{4KTR_s} \quad \text{Double-balanced}
\end{aligned}$$

4. SIMULATION RESULTS

A single-balanced mixer was designed and simulated in 0.18 μ m CMOS process. To include the gate-induced noise parameters in the simulations, the BMIS4 level 54 MOS model was utilized. The output noise, and the NF of the mixer was derived using three approaches; HSPICE simulation, the analytical model developed in [1], and the proposed analytical model in this paper. Fig. 6 (a) depicts the results obtained by these three approaches for three distinct LO voltage amplitudes. For lower frequencies, the noise estimations given by this paper simplifies to those given by [1]. As LO frequency goes beyond few hundred MHz, [1] results in large error because gate-induced noise becomes dominant. Fig. 6 (b) depicts the NF variation with respect to the LO frequency. As clearly seen from Figs. 6 (a) and (b), [1] is incapable of predicting the frequency-dependence of the total output noise and the NF at high frequencies, whereas the proposed noise model is accurately following the simulations. Fig. 7 demonstrates the variation of the noise contribution of the switching pair in terms of the RF transconductor for the designed single-balanced mixer at the LO frequency of 2.4GHz.

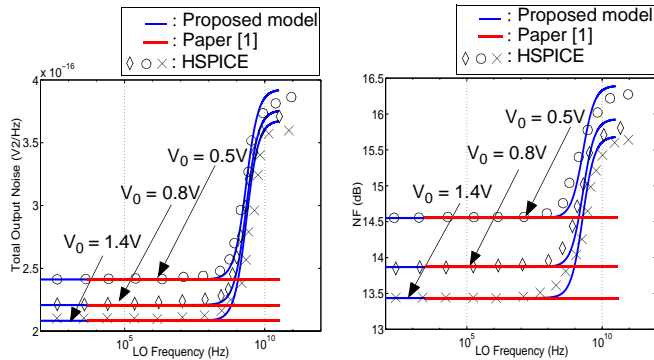


Fig. 6. (a) Total output noise vs. LO frequency, (b) the noise figure (dB) vs. the LO frequency

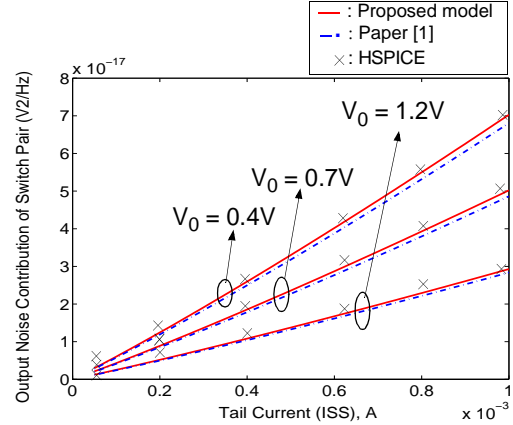


Fig. 7. Switching pair output noise contribution vs. tail current

5. CONCLUSIONS

A new analytical model for high-frequency noise in RF active CMOS mixers was presented. The analysis included the contribution of non-white gate-induced noise at the output and the spot noise figure (NF) of the RF CMOS mixer, while accounting for the non-zero correlation between the gate-induced noise and the channel thermal noise. The analytical model predicts that the output noise and NF are both a strong function of the LO frequency at gigahertz range of frequencies. Simulation results verify the accuracy of the analytical model.

6. REFERENCES

- [1] M. T. Terrovitis, R. G. Meyer, "Noise in Current-Commuting CMOS Mixers," *IEEE J. Solid-State Circuits*, vol. 34, No. 6, pp. 772-783, June 1999.
- [2] H. Darabi, A. A. Abidi, "Noise in RF CMOS Mixers: A Simple Physical Model," *IEEE J. Solid-State Circuits*, vol. 35, No. 1, pp. 15-25, Jan. 2000.
- [3] D. Ham, A. Hajimiri, "Complete Noise Analysis for CMOS Switching Mixers Via Stochastic Differential Equations," *IEEE CICC*, pp. 439-442, May 2000.
- [4] Y. Tsidividis, *Operation and Modeling of the MOS Transistor*, pp. 440-512, McGraw-Hill, 1999.
- [5] B. Razavi, R-H. Yan, K. F. Lee, "Impact of distributed gate resistance on the performance of MOS devices, *IEEE TCAS I: Fundamental Theory and Applications*, Vol. 41, no. 11, pp. 750-754, Nov 1994.
- [6] T. H. Lee, *The Design of CMOS Radio-Frequency Integrated Circuits*, Cambridge University Press, 1998.
- [7] Papoulis, A., *Probability, Random Variables, and Stochastic Processes*, McGraw-Hill, 1991.
- [8] Oppenheim, A. V., Willsky, A. S., Young, I. T., *Signals and Systems*, Prentice-Hall, Inc., 1983.
- [9] A. Hajimiri, T. H. Lee, "A General Theory of Phase Noise in Electrical Oscillators," *IEEE I. Solid-State Circuits*, vol. 33, No. 2, Feb. 1998.
- [10] B. Razavi, *RF Microelectronics*, Prentice-Hall, 1998.