

Assignment 5

1. Examine the “perfect” JPEG Encoder source code
 - `/home/doemer/EECS222C_F08/jpegencoder.sc`
2. Examine the “perfect” JPEG Encoder model in SCE
 - Setup
 - Same as before (use SCE version 20080601)
 - Browse the structural hierarchy
 - View the hierarchy chart
 - Validate the model (compile and simulate)
 - Profile, analyze, estimate the model
 - For a single ARM_7TDMI CPU
 - For complexity of “Computation”
 - Deliverables
 - Bar graph of Computation Profile
 - `“ARM7TDMI.ps”`
 - Due
 - by Friday, Nov 7, 2008, at noon
 - by email to `doemer@uci.edu` with subject “EECS222C HW5”