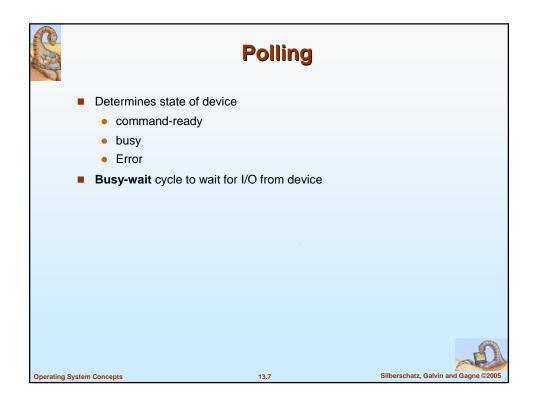
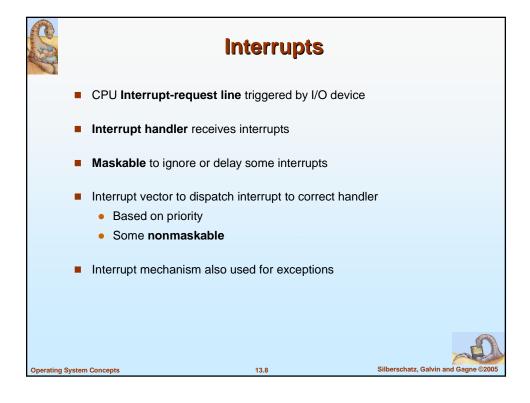
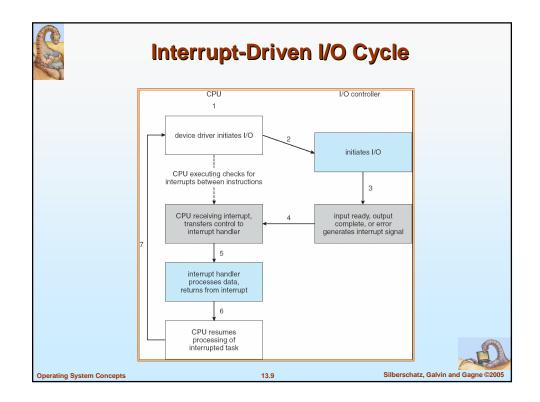


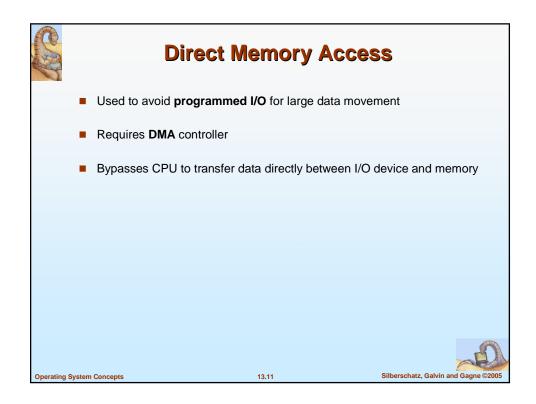
I/O address range (hexadecimal)	device
000-00F	DMA controller
020–021	interrupt controller
040–043	timer
200–20F	game controller
2F8–2FF	serial port (secondary)
320–32F	hard-disk controller
378–37F	parallel port
3D0-3DF	graphics controller
3F0–3F7	diskette-drive controller
3F8–3FF	serial port (primary)

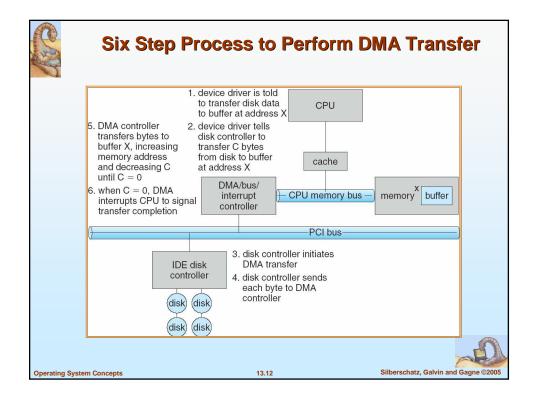


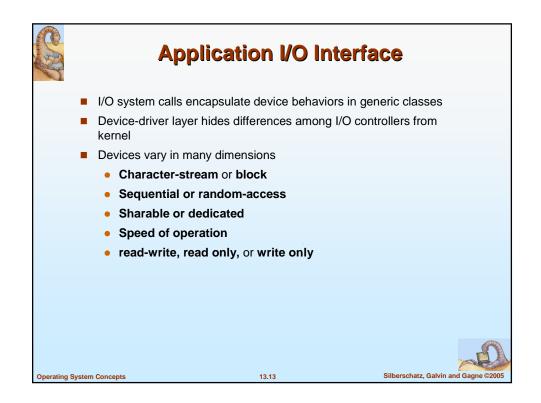


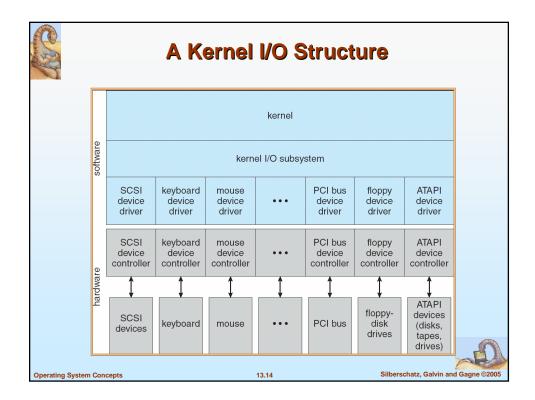


Intel F	Pentium Pro	cessor Event-Vector Ta	able
	vector number	description	
	0	divide error	
	1	debug exception	
	2	null interrupt	
	3	breakpoint	
	4	INTO-detected overflow	
	5	bound range exception	
	6	invalid opcode	
	7	device not available	
	8	double fault	
	9	coprocessor segment overrun (reserved)	
	10	invalid task state segment	
	11	segment not present	
	12	stack fault	
	13	general protection	
	14	page fault	
	15	(Intel reserved, do not use)	
	16	floating-point error	
	17	alignment check	
	18	machine check	
	19–31	(Intel reserved, do not use)	_
	32–255	maskable interrupts	1
			nt
perating System Concepts		13.10 Silberschatz, Galvin a	nd Gagne ©20









aspect	variation	example
data-transfer mode	character block	terminal disk
access method	sequential random	modem CD-ROM
transfer schedule	synchronous asynchronous	tape keyboard
sharing	dedicated sharable	tape keyboard
device speed	latency seek time transfer rate delay between operations	
I/O direction	read only write only read–write	CD-ROM graphics controller disk

