

Homework Assignment 1

- Administration
 - Server
 - `epsilon.eecs.uci.edu`
 - Intel Pentium CPU, 3.0 GHz, 1GB RAM
 - RedHat Linux (Fedora Core 4)
 - Access via secure shell protocol (`ssh`)
 - Accounts
 - User ID same as your UCI net ID
 - Password as discussed in class
 - SpecC Software (© by CECS, UCI)
 - SpecC Compiler and Simulator
 - `source /opt/sce-20080601/bin/setup.csh`

EECS222A: SoC Description and Modeling, Lecture 2

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Homework Assignment 1

- Task: Introduction to SpecC Compiler and Simulator
 - Become familiar with `scc`
 - See `man scc` for manual page
 - Use `scc` to compile and simulate the examples in
 - `/opt/sce-20080601/examples/simple/`
 - Build and simulate the sender/receiver example
 - See Slide 25! (behavior `B` should be `Main`)
 - Sender `s` should send values 0.0, 0.5, ... 5.0 to the receiver `R` which prints them to the screen
- Deliverables
 - Source file: `SendReceive.sc`
 - Simulation log: `SendReceive.log`
- Due
 - By next week: October 9, 2009, 12pm (noon)
 - Email to `doemer@uci.edu` with subject "EECS222A Assignment 1"

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