

## Final Technical Report

- Title
  - SoC Software Synthesis for a MP3 Audio Decoder
    - Final Technical Report for EECS 222C, Spring 2013
- Author
  - Your Name and ID
- Contents
  - Describe the overall SoC design approach
  - Outline the major steps in the design flow
  - Use the MP3 decoder as case study
    - Tell the story of the project assignments!
  - Conclude with a summary of the lessons learned
- Length
  - About 12 pages (including title page and references)

## Final Technical Report

- Suggested Outline
  1. Title page with abstract
  2. Introduction
    1. Top-down embedded software design flow
  3. Case study on a MP3 decoder
    1. Application reference code
    2. System specification model
    3. Validation and profiling
    4. Target architecture selection
    5. Transaction Level Model (TLM)
    6. C code generation and cross-compilation
    7. Pin-Accurate Model (PAM)
    8. Instruction Set Simulation (ISS) Model
  4. Conclusion
    1. Lessons learned
  5. References

## Course Administration

- Final Exam
  - Date and time
    - **Wednesday, June 12, 2013, until 12pm (noon)**
  - Format
    - Delivery of Final Technical Report
    - Electronic submission (**turnin** on server)
      - `final/ISS.sir`
      - `final/Report.pdf`
- **Hard deadline!**
  - **Wednesday, June 12, 2013, 12pm (noon)**