

ECPS 203

Embedded Systems Modeling and Design

Lecture 8

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Lecture 8: Overview

- SystemC: From the Ground Up (Part 3)
 - Elaboration and simulation phases
 - SystemC scheduler
 - Channels and interfaces
 - Ports and exports
 - Bus modeling

IEEE SystemC Language

- SystemC: From the Ground Up (Part 3)
 - **DAC15_SystemC_Training.pdf**, slides 44 through 63
by David Black, Doulos
 - SystemC training day at Design Automation Conference 2015
 - *“The Definitive Guide to SystemC: The SystemC Language”*
 - Core Concepts and Syntax
 - Elaboration and simulation phases
 - SystemC scheduler
 - Channels and interfaces
 - Ports and exports
 - Bus Modeling
 - Master and slave interfaces
 - Blocking versus non-blocking