

ECPS 203

Embedded Systems Modeling and Design

Lecture 9

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Lecture 9: Overview

- Course Administration
 - Midterm course evaluation
- SystemC Simulation Semantics
 - Motivating Examples
 - Discrete Event Simulation Algorithm
- Project Discussion
 - Status and next steps
 - Assignment 4

Course Administration

- Midterm Course Evaluation
 - One week, starting today!
 - Monday, Oct. 30, 8am – Friday, Nov. 3, 11pm
 - Online via EEE Evaluation application
- Feedback from students to instructors
 - Completely voluntary
 - Completely anonymous
 - Very valuable
 - Help to improve this class!
- Mandatory Final Course Evaluation
 - expected for week 10 (TBA)

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SystemC Simulation Semantics

- Motivating Example 1
 - Given:

<pre>SC_MODULE(Top) { int x; void th1(void); void th2(void); SC_CTOR(Top) { th1(); th2(); } };</pre>	<pre>void Top::th1(void) { x = 5; };</pre>	<pre>void Top::th2(void) { x = 6; };</pre>
--	--	--
 - What is the value of **x** at the end of simulation?
 - **Answer: x = 6**

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SystemC Simulation Semantics

- Motivating Example 2

- Given:

```
SC_MODULE(Top)
{
    int x;

    void th1(void);
    void th2(void);

    SC_CTOR(Top)
    { SC_THREAD(th1);
      SC_THREAD(th2);
    }
};
```

```
void Top::th1(void)
{
    x = 5;
};
```

```
void Top::th2(void)
{
    x = 6;
};
```

- What is the value of **x** at the end of simulation?

- Answer: The model is non-deterministic!
x may have the value 5 or 6.

SystemC Simulation Semantics

- Motivating Example 3

- Given:

```
SC_MODULE(Top)
{
    int x;

    void th1(void);
    void th2(void);

    SC_CTOR(Top)
    { SC_THREAD(th1);
      SC_THREAD(th2);
    }
};
```

```
void Top::th1(void)
{
    wait(10, SC_NS);
    x = 5;
};
```

```
void Top::th2(void)
{
    x = 6;
};
```

- What is the value of **x** at the end of simulation?

- Answer: **x = 5**

SystemC Simulation Semantics

- Motivating Example 4

– Given:

```
SC_MODULE(Top)
{
    int x;

    void th1(void);
    void th2(void);

    SC_CTOR(Top)
    { SC_THREAD(th1);
      SC_THREAD(th2);
    }
};
```

```
void Top::th1(void)
{
    wait(10, SC_NS);
    x = 5;
};
```

```
void Top::th2(void)
{
    wait(10, SC_NS);
    x = 6;
};
```

– What is the value of **x** at the end of simulation?

– Answer: The model is non-deterministic!
x may have the value 5 or 6.

SystemC Simulation Semantics

- Motivating Example 5

– Given:

```
SC_MODULE(Top)
{
    int x;
    sc_event e;
    void th1(void);
    void th2(void);

    SC_CTOR(Top)
    { SC_THREAD(th1);
      SC_THREAD(th2);
    }
};
```

```
void Top::th1(void)
{
    x = 5;
    e.notify();
};
```

```
void Top::th2(void)
{
    wait(e);
    x = 6;
};
```

– What is the value of **x** at the end of simulation?

– Answer: The model is non-deterministic!
x may have the value 5 or 6
 (immediate notification may get lost!)

SystemC Simulation Semantics

- Motivating Example 6

– Given:

```
SC_MODULE(Top)
{
    int x;
    sc_event e;
    void th1(void);
    void th2(void);

    SC_CTOR(Top)
    { SC_THREAD(th1);
      SC_THREAD(th2);
    }
};
```

```
void Top::th1(void)
{
    x = 5;
    e.notify(
        SC_ZERO_TIME);
};
```

```
void Top::th2(void)
{
    wait(e);
    x = 6;
};
```

– What is the value of **x** at the end of simulation?

– Answer: **x = 6**

SystemC Simulation Semantics

- Motivating Example 7

– Given:

```
SC_MODULE(Top)
{
    int x;
    sc_event e;
    void th1(void);
    void th2(void);

    SC_CTOR(Top)
    { SC_THREAD(th1);
      SC_THREAD(th2);
    }
};
```

```
void Top::th1(void)
{
    e.notify(
        SC_ZERO_TIME);
    x = 5;
};
```

```
void Top::th2(void)
{
    wait(e);
    x = 6;
};
```

– What is the value of **x** at the end of simulation?

– Answer: **x = 6**

SystemC Simulation Semantics

- Motivating Example 8

– Given:

```
SC_MODULE(Top)
{
    int x;
    sc_event e;
    void th1(void);
    void th2(void);

    SC_CTOR(Top)
    { SC_THREAD(th1);
      SC_THREAD(th2);
    }
};
```

```
void Top::th1(void)
{
    wait(10, SC_NS);
    x = 5;
    e.notify(
        SC_ZERO_TIME);
};
```

```
void Top::th2(void)
{
    wait(e);
    x = 6;
};
```

– What is the value of **x** at the end of simulation?

– Answer: **x = 6**

SystemC Simulation Semantics

- Motivating Example 9

– Given:

```
SC_MODULE(Top)
{
    int x;
    sc_event e;
    void th1(void);
    void th2(void);

    SC_CTOR(Top)
    { SC_THREAD(th1);
      SC_THREAD(th2);
    }
};
```

```
void Top::th1(void)
{
    x = 5;
    e.notify(
        SC_ZERO_TIME);
};
```

```
void Top::th2(void)
{
    wait(10, SC_NS);
    wait(e);
    x = 6;
};
```

– What is the value of **x** at the end of simulation?

– Answer: **x = 5**

Thread **th2** never completes,
notified event **e** expires and is lost!

SystemC Simulation Semantics

- Discrete Event Simulation (DES) Algorithm
 - described in SystemC LRM (but noted in a different format)
 - ⇒ abstract definition defines a set of valid implementations
 - ⇒ intentionally defined with non-deterministic thread ordering
- Definitions:
 - At any time, each thread t is in one of the following sets:
 - **READY**: set of threads ready to execute (aka. **RUNNABLE**)
 - **WAIT**: set of threads suspended by `wait(event)`
 - **WAITTIME**: set of threads suspended by `wait(time)`
 - Notified events are stored in a set **N**
 - `notify e1` adds event $e1$ to **N**
 - `wait e1` will wakeup when $e1$ is in **N**
 - Consumption of event e means event e is taken out of **N**
 - Expiration of notified events means **N** is set to \emptyset

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SystemC Simulation Semantics

- Discrete Event Simulation (DES) Algorithm

```

graph TD
    Start([Start]) --> Select[Select thread t ∈ READY, execute t]
    Select --> Notify{notify}
    Notify -- YES --> AddN[Add notified events to N]
    AddN --> WaitE{wait(e)}
    WaitE -- YES --> MoveWaitE[Move t ∈ READY to WAIT]
    MoveWaitE --> WaitT{wait(t)}
    WaitT -- YES --> MoveWaitT[Move t ∈ READY to WAITTIME]
    MoveWaitT --> ReadyEmpty1{READY = ∅}
    ReadyEmpty1 -- NO --> Select
    ReadyEmpty1 -- YES --> MoveWait[Move all t ∈ WAIT waiting for events e ∈ N to READY]
    MoveWait --> SetN[Set N = ∅]
    SetN --> ReadyEmpty2{READY = ∅}
    ReadyEmpty2 -- NO --> Select
    ReadyEmpty2 -- YES --> Update[Update simulation time, move earliest t ∈ WAITTIME to READY]
    Update --> ReadyEmpty3{READY = ∅}
    ReadyEmpty3 -- YES --> Stop([Stop])
    ReadyEmpty3 -- NO --> Select
    
```

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SystemC Simulation Semantics

- Discrete Event Simulation (DES)
 - Concurrent threads of execution
 - Managed by a central scheduler
 - Driven by events and time advances
 - Delta cycle
 - Time cycle
 - Partial temporal order with barriers
- Reference Simulator
 - IEEE SystemC specifies cooperative multi-threading
 - A single thread is active at any time (even if multiple cores are available)
 - Example: Execution of four threads

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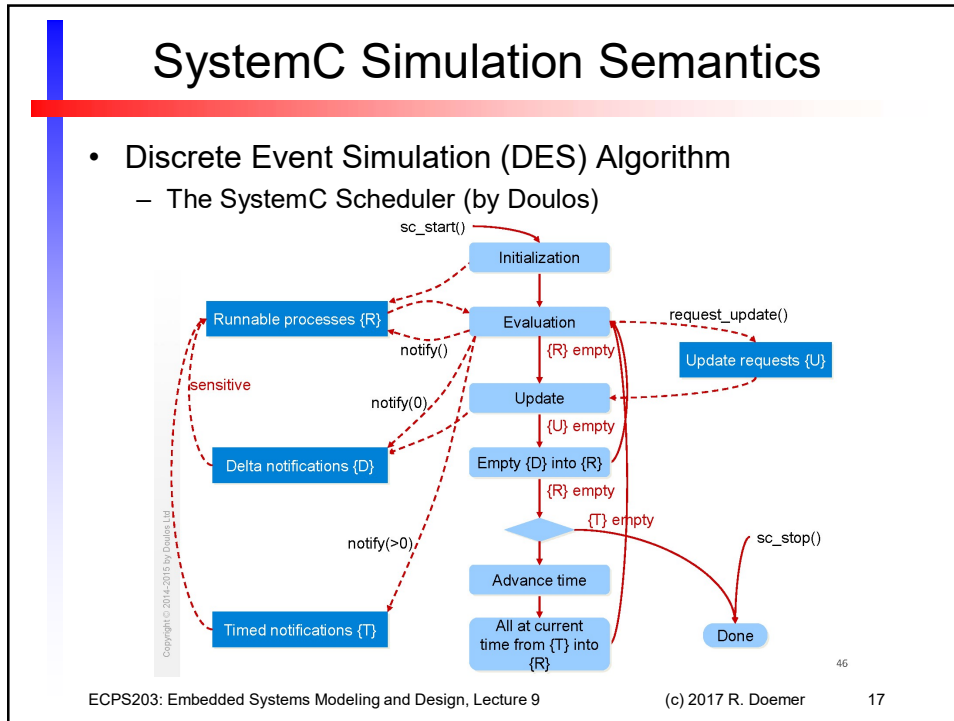
SystemC Simulation Semantics

- Accellera SystemC Proof-of-Concept Library
 - uses an extra **root thread** for the following tasks:
 - Elaboration phase
 - Scheduling
 - Event notifications
 - Channel updates
 - Delta cycle updates
 - Simulation time updates
 - SC_METHOD calls
 - (not shown)

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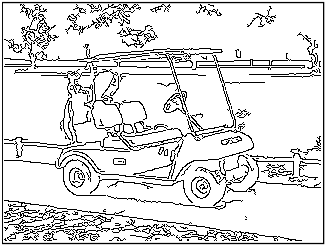


ECPS 203 Project

- Application Example: Canny Edge Detector
 - Embedded system model for image processing: Automatic edge detection in a digital camera



golfcart.pgm



golfcart.pgm_s_0.60_l_0.30_h_0.80.pgm

- Application source and documentation:
 - http://marathon.csee.usf.edu/edge/edge_detection.html
 - http://en.wikipedia.org/wiki/Canny_edge_detector

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Project Assignment 1

- Task: Introduction to Application Example
 - Canny Edge Detector
 - Algorithm for edge detection in digital images
- Steps
 1. Setup your Linux programming environment
 2. Download, adjust, and compile the application C code with the GNU C compiler (`gcc`)
 3. Study the application, determine function-call tree
- Deliverables
 - Source code and text file: `canny.c`, `canny.txt`
- Due
 - Wednesday, next week: October 11, 2017, 6pm

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Project Assignment 2

- Task: Clean C++ model with static memory allocation
 - Prepare the C++ source code for modeling in SystemC
 - Configure parameters for specific application
 - Apply static memory allocation
- Steps
 1. Fix the off-by-one bug in the `non_max_supp` function
 2. Clean-up the code for compilation without warnings
 3. Fix configuration parameters to compile-time constants
 4. Remove or replace dynamic memory allocation
- Deliverables
 - Source code and text file: `canny.cpp`, `canny.txt`
- Due
 - Wednesday, next week: October 18, 2017, 6pm

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ECPS 203 Project

- Capture Video Footage of Engineering Buildings
 - Drone flight above UCI Engineering (image by Google Maps)



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ECPS 203 Project

- Capture Video Footage of Engineering Buildings
 - Drone flight demonstration



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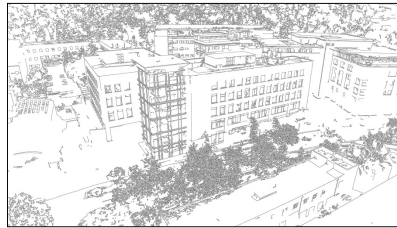
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ECPS 203 Project

- Application Example: Canny Edge Detector
 - Embedded system model for image processing:
Automatic edge detection in a digital video camera



Engineering012.png



Engineering012_edges.pgm

- Video taken by a drone flying over UCI Engineering Plaza
 - Available on the server: `~ecps203/public/DroneFootage/`
 - High resolution, 2704 by 1520 pixes
 - Representative sample, using 30 extracted frames for test bench model

Project Assignment 4

- Task: From Single Image to Video Stream Processing
 - Prepare a sequence of image frames from the video
 - Convert the Canny application the selected video frames
- Steps
 1. Extract 30 of video frames suitable for use in a test bench
 2. Convert the color frames to grey-scale images in PGM format
 3. Recode your Canny C++ model to process the video frames
 - To run Canny application successfully, increase stack size
- Deliverables
 - Source code and text file: **Canny.cpp**, **Canny.txt**
- Due
 - Wednesday, November 1, 2017, 6pm