

ECPS 203

Embedded Systems Modeling and Design

Lecture 10

Rainer Dömer

doemer@uci.edu

Center for Embedded and Cyber-physical Systems
University of California, Irvine



Lecture 10: Overview

- SystemC: From the Ground Up (Part 4)
 - Bus modeling
 - Odds and ends

IEEE SystemC Language

- SystemC: From the Ground Up (Part 4)
 - **DAC15_systemC_Training.pdf**, slides 57 through 78
by David Black, Doulos
 - SystemC training day at Design Automation Conference 2015
 - *“The Definitive Guide to SystemC: The SystemC Language”*
 - Bus Modeling
 - Master and slave interfaces
 - Blocking versus non-blocking
 - Odds and Ends
 - Concurrent assignments to signals, resolved signals
 - Event methods
 - Command argument access
 - Error reporting