EECS 10: Computational Methods in Electrical and Computer Engineering Lecture 15

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Lecture 15: Overview

- Course Administration
 - Final course evaluation
- Basic Computer Architecture
 - Computer components
- Binary Data Representation
 - Bits, bytes, and words
 - Memory sizes
 - Number systems
 - Memory organization
- Objects in memory

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Course Administration

- · Final Course Evaluation
 - Open three weeks
 - Nov. 21, 2019, through Sunday, Dec. 8, 2019
 - Online via EEE Evaluation application
- Mandatory Evaluation of Course and Instructor
 - Voluntary
 - Anonymous
 - Very valuable
 - · Help to improve this class!
- Please spend 5 minutes!

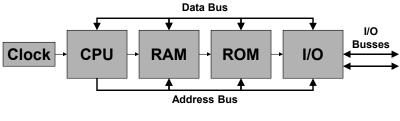
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Basic Computer Architecture

- Essential Computer Components
 - Central Processing Unit (CPU)
 - e.g. Intel Pentium, Motorola PowerPC, Sun SPARC, ...
 - Random Access Memory (RAM)
 - storage for program and data, read and write access
 - Read Only Memory (ROM)
 - · fixed storage for basic input/output system (BIOS)
 - I/O Units
 - · Input/output interfaces connecting to peripherals

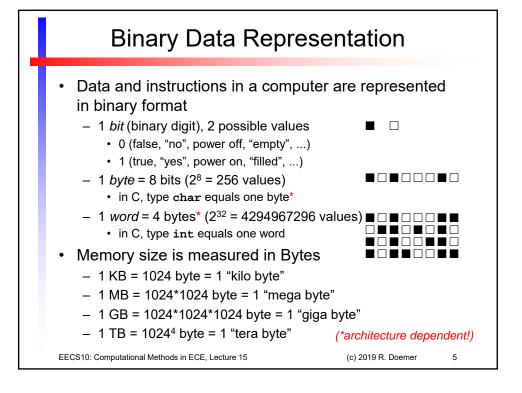


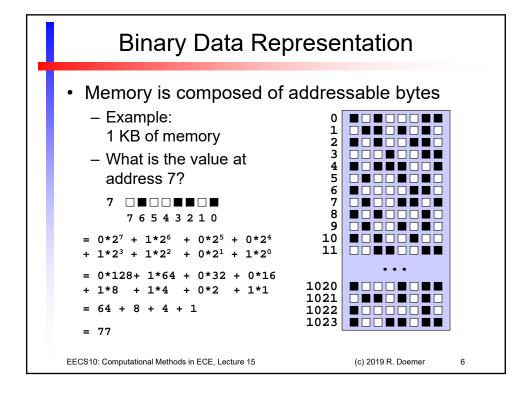
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Binary Data Representation

- Review: Number Systems
 - DEC: Decimal numbers
 - Base 10, digits 0, 1, 2, 3, ..., 9
 - e.g. $157 = 1*10^2 + 5*10^1 + 7*10^0$
 - BIN: Binary numbers
 - Base 2, digits 0, 1
 - e.g. $100111101_2 = 1*2^7 + 0*2^6 + 0*2^5 + 1*2^4 + ... + 1*2^0$
 - OCT: Octal numbers
 - Base 8, digits 0, 1, 2, 3, ..., 7
 - e.g. $235_8 = 2*8^2 + 3*8^1 + 5*8^0$
 - HEX: Hexadecimal numbers
 - Base 16, digits 0, 1, 2, 3, ..., 9, A, B, C, ..., F
 - e.g. $9D_{16} = 9*16^1 + 13*16^0$

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Binary Data Representation

• Review: Number Systems

DEC	BIN	OCT	HEX
0	0000	0	0
1	0001	1	1
2	0010	2	2
3	0011	3	3
4	0100	4	4
5	0101	5	5
6	0110	6	6
7	0111	7	7
8	1000	10	8
9	1001	11	9
10	1010	12	A
11	1011	13	В
12	1100	14	C
13	1101	15	D
14	1110	16	E
15	1111	17	F

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Binary Data Representation

Review: Number Systems (signed/unsigned)

SDEC	UDEC	BIN	OCT	HEX
_	-			
0	0	0000	0	0
1	1	0001	1	1
2	2	0010	2	2
3	3	0011	3	3
4	4	0100	4	4
5	5	0101	5	5
6	6	0110	6	6
7	7	0111	7	7
-8	8	1000	10	8
-7	9	1001	11	9
-6	10	1010	12	A
-5	11	1011	13	В
-4	12	1100	14	C
-3	13	1101	15	D
-2	14	1110	16	E
-1	15	1111	17	F
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Binary Data Representation

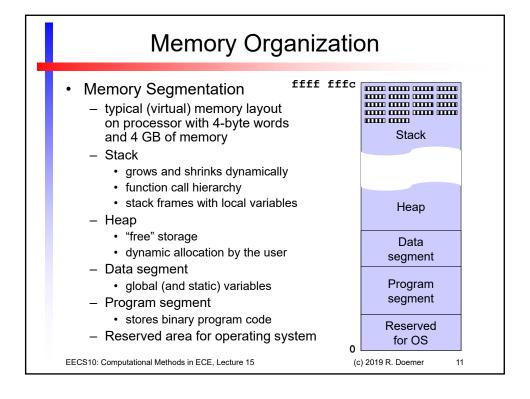
- Review: Number Systems
 - Signed representation: two's complement
 - to obtain the negative of any number in binary representation, ...
 - ... invert all bits,
 - ... and add 1
 - Example: 4-bit two's complement

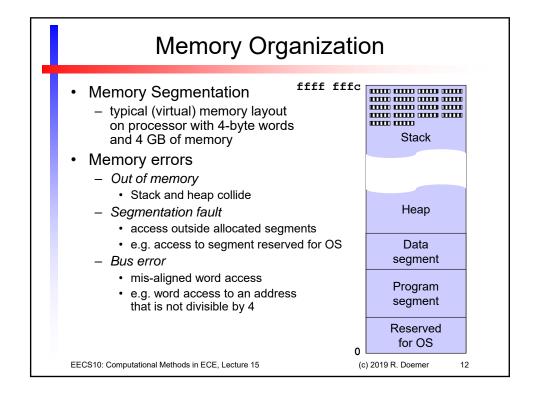
HEX	OCT	BIN	UDEC	SDEC
• • •	• • •	• • •	• • •	• • •
7	7	0111	7	7
8	10	1000	8	-8
9	11	1001	9	-7
• • •	• • •	• • •	• • •	• • •

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Objects in Memory

- Data in memory is organized as a set of objects
- Every object has ...
 - ... a type (e.g. int, double, char[5])
 - · type is known to the compiler at compile time
 - ... a value (e.g. 42, 3.1415, "text")
 - · value is used for computation of expressions
 - ... a size (number of bytes in the memory)
 - in C, the sizeof operator returns the size of a variable or type
 - ... a location (address in the memory)
 - in C, the "address-of" operator (&) returns the address of an object
- Variables ...
 - ... serve as identifiers for objects
 - ... are bound to objects
 - ... give objects a name

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Objects in Memory

Example: Variable values, addresses, and sizes

```
int x = 42;
int y = 13;
char s[] = "Hello World!";

printf("Value of x is %d.\n", x);
printf("Address of x is %p.\n", &x);
printf("Size of x is %u.\n", sizeof(x));
printf("Value of y is %d.\n", y);
printf("Address of y is %p.\n", &y);
printf("Size of y is %u.\n", sizeof(y));
printf("Value of s is %s.\n", s);
printf("Value of s is %s.\n", s);
printf("Size of s is %u.\n", sizeof(s));
printf("Value of s[1] is %c.\n", s[1]);
printf("Address of s[1] is %p.\n", &s[1]);
printf("Size of s[1] is %u.\n", sizeof(s[1]));
```

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