# The Definitive Guide to SystemC: The SystemC Language

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# Track 3: The Definitive Guide to SystemC The SystemC Language



- Introduction to SystemC
- Core Concepts and Syntax

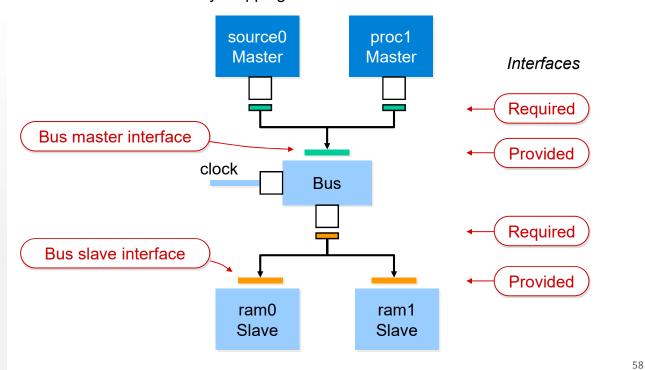


- Bus Modeling
  - Master and slave interfaces
  - Blocking versus non-blocking
  - Multiports
- Odds and Ends

### **Example Bus Model**



Multiple bus masters (modules), shared bus (channel), multiple slaves (channels) Bus arbitration and memory mapping built into the bus



#### **Master Interface Definition**



```
source0
Master
id = 0

clock

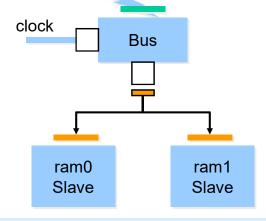
Bus
```

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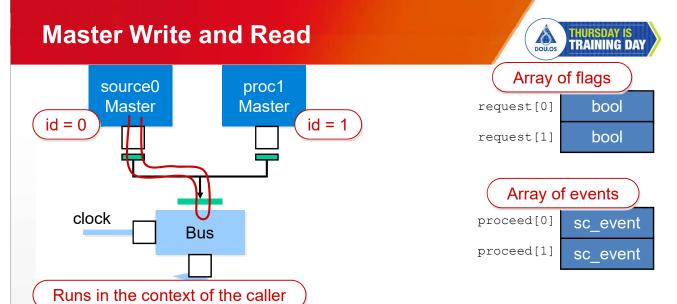
# **Slave Interface Definition**





```
class slave_if : virtual public sc_interface
{
public:
    virtual void slave_write(sc_uint<8> address, sc_uint<12> data) = 0;
    virtual void slave_read (sc_uint<8> address, sc_uint<12> &data) = 0;
    virtual void get_map(unsigned int &start, unsigned int &size) = 0;
};

Memory map managed within bus channel
```



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# **Blocking and Non-blocking Calls**



An Interface Method Call runs in the context of the caller

Important!

ASI terminology:

- A blocking method may call wait
- A blocking method must be called from a thread process
- A non-blocking method must not call wait
- A non-blocking method may be called from a thread or method process
- Naming convention nb\_\*

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#### **Bus Controller Process**



```
void Bus::control_bus()
{
   int highest;
   for (;;)
   {
      wait(clock->posedge_event());

      // Pick out a master that's made a request
      highest = -1;
      for (int i = 0; i < n_masters; i++)
            if (request[i])
                highest = i;

      // Notify the master with the highest id
      if (highest > -1)
            proceed[highest].notify();
    }
}
```

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